

In the claims:

This list of claims will replace all the prior version, and listings, of claims in the application.

LIST OF CLAIMS

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34. (currently amended) An emission electron source comprising:

a cathode electrode disposed on a substrate, the cathode electrode for providing a source of electrons;

10 an emitter layer disposed over said cathode electrode and formed from a composition of an embedding material and one or a plurality of nano-structures embedded therein, the emitter layer having a surface, portions of the nano-structures being truncated parallel to the surface and ~~having portions~~ protruding above the surface to emit electrons;

an insulator disposed over the emitter layer, the insulator having one or a plurality of apertures, each exposing at least the ends of the nano-structures in the emitter layer; and

15 a gate electrode disposed over the insulator and having one or a plurality of apertures, wherein each aperture exposes a single nano-structure and is concentrically self-aligned with the end of ~~one of the nano-structures, so as to expose a single nano-structure and provide the nano-~~ ~~structures with substantially the same emitter to gate distance,~~ the gate electrode being operative to control the emission of electrons through the apertures from the exposed nano-structures;

20 wherein the nano-structures have a coating for enhanced field emission performance.

35. (currently amended) An emission electron source comprising:

a cathode electrode disposed on a substrate, the cathode electrode for providing a source of electrons;

25 an emitter layer disposed over said cathode electrode and formed from a composition of an embedding material and one or a plurality of nano-structures embedded therein, the emitter layer having a surface, portions of the nano-structures being truncated parallel to the surface and ~~having portions~~ protruding above the surface to emit electrons;

30 an insulator disposed over the emitter layer, the insulator having one or a plurality of apertures, each exposing at least the ends of the nano-structures in the emitter layer; and

a gate electrode disposed over the insulator and having one or a plurality of apertures, wherein each aperture exposes a single nano-structure and is concentrically self-aligned with the end of ~~one of the nano-structures, so as to expose a single nano-structure and provide the nano-~~ structures with substantially the same emitter-to-gate distance, the gate electrode being operative
5 to control the emission of electrons through the apertures from the exposed nano-structures;

36. (previously presented) An electron source as recited in claim 35, wherein said nano-structures are substantially vertical.

10 37. (previously presented) An electron source as recited in claim 35, wherein said nano-structures are individually spaced apart.

38. (previously presented) An electron source as recited in claim 35, wherein said emitter-to-gate distance for each nano-structure is substantially less than one micrometer.

15 39. (previously presented) An electron source as recited in claim 35, wherein the nano-structures have a surface density substantially higher than $10^6/\text{cm}^2$.

20 40. (previously presented) An electron source as recited in claim 35, wherein the nano-structures protrude above the surface of the emitting layer for not more than half of one micrometer.

41. (previously presented) An electron source as recited in claim 35, wherein the apertures in the insulator expose the entire protrusion portion of the nano-structures in the emitting layer.

25 42. (previously presented) An electron source as recited in claim 35, wherein the nano-structures have at least one of their three dimensions in the nanometer range.

43. (previously presented) An electron source as recited in claim 35, wherein the nano-structures include nano-tubes, nano-wires, nano-fibers, and nano-cones.

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44. (previously presented) An electron source as recited in claim 35, wherein the nano-structures have a coating for enhanced field emission performance.

45. (previously presented) An electron source as recited in claim 35, wherein the nano-structures are selected from a group of materials consisting of carbon, refractory metals and alloys, conductive ceramics, conductive ceramic composites, and doped semiconductors.

46. (previously presented) An electron source as recited in claim 45, wherein the carbon includes carbon nano-tube, carbon nano-fiber, and carbon nano-cone.

47. (previously presented) An electron source as recited in claim 35, wherein the nano-structures comprise a nonconductive core and a conductive shell.

48. (previously presented) An electron source as recited in claim 47, wherein the nonconductive core is made from one of wide band gap semiconductors, including diamond, BN, AlN, AlGaN, GaN, GaAs, SiC, and ZnO.

49. (previously presented) An electron source as recited in claim 35, wherein the embedding material is comprised of at least two layers.

50. (previously presented) An electron source as recited in claim 49, wherein the first layer of the embedding material is conductive.

51. (previously presented) An electron source as recited in claim 35, wherein the insulator and the embedding material are composed of the same dielectric material.

52. (previously presented) An electron source as recited in claim 35, wherein said insulator functions also as the embedding material.

53. (previously presented) An electron source as recited in claim 35,

wherein the cathode electrode is configured as a plurality of electrically isolated cathode electrodes, each for supplying an independent source of electrons;

wherein the gate electrode is configured as a plurality of electrically isolated electrodes, each intersecting with said cathode electrodes and having one or a plurality of apertures at each intersections, each gate electrode being operative to control the emission of electrons through the apertures along the gate electrode; and

wherein activation of a selected cathode and a selected gate electrode determines an intersection where the nano-structures emit electrons.

10 54. (previously presented) An electron source comprising:

a substrate;

electrode means, disposed over the substrate, for providing a source of electrons;

means, disposed over the source means, for emitting electrons provided by the source means, the emitting means including a one or a plurality of nano-structure emitting means for providing a flow of electrons and means for supporting the nano-structure emitting means;

an insulator disposed over the emitting means; and

one or a plurality of gating means, disposed over the insulator, for controlling the flow of electrons emitted by the nano-structure emitting means, each of said gating means arranged symmetrically relative to one of the nano-structure emitting means.

20 55. (previously presented) An electron source as recited in claim 54, wherein the insulator and the gating means each include one or more apertures that expose the nano-structure emitting means.

25 56. (previously presented) An electron source as recited in claim 54, wherein the nano-structure emitting means has at least one of its three dimensions in the nanometer range.

57. (Previously presented) An electron source as recited in claim 54, wherein the nano-structure emitting means includes carbon nano-tube, carbon nano-fiber, and carbon nano-cones.

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58. (Previously presented) An electron source as recited in claim 54, wherein the nano-structure emitting means is substantially vertical.

59. (Previously presented) An electron source as recited in claim 54, wherein the nano-structure emitting means is an array of individually spaced apart nano-structures.

60. (currently amended) A display comprising:

an electron source that includes:

a cathode electrode disposed on a substrate, the cathode electrode for providing a source of electrons;

an emitter layer disposed over said cathode electrode and formed from a composition of an embedding material and one or a plurality of nano-structures embedded therein, the emitter layer having a surface, portions of the nano-structures being truncated parallel to the surface and having portions protruding above the surface to emit electrons;

an insulator disposed over the emitter layer, the insulator having one or a plurality of apertures, each exposing at least the ends of the nano-structures in the emitter layer; and

a gate electrode disposed over the insulator and having one or a plurality of apertures, wherein each aperture exposes a single nano-structure and is concentrically self-aligned with the end of one of the nano-structures, so as to expose a single nano-structure and provide the nano-structures with substantially the same emitter to gate distance, the gate electrode being operative to control the emission of electrons through the apertures from the exposed nano-structures; and

an anode plate including a transparent anode electrode disposed over a glass substrate and a phosphor screen disposed over the anode electrode, the anode plate being positioned opposite to said electron source with a vacuum gap disposed therebetween;

wherein electrons are emitted from said nano-structures by applying a voltage between said cathode and gate electrodes, and are made incident on said phosphor screen to make luminous said phosphor screen.

61. (Previously presented) A display as recited in claim 60, wherein the nano-structures are substantially vertical.

62. (Previously presented) A display as recited in claim 60, wherein the emitter-to-gate distance for each emitter is substantially less than one micrometer.

5 63. (Previously presented) A display as recited in claim 60, wherein the nano-structures have a surface density substantially higher than $10^6/\text{cm}^2$.

64. (Previously presented) A display as recited in claim 60,
wherein the cathode electrode is configured as a plurality of strip-like cathode electrodes
10 extending substantially in the same direction in such a manner as to be spaced from each other at intervals in the transverse direction, each cathode strip for providing an independent source of electrons;

wherein the gate electrode is configured as a plurality of strip-like gate electrodes
extending in such a manner as to intersect said plurality of cathode electrodes and to be spaced
15 from each other at intervals in the transverse direction, and having one or a plurality of apertures at each intersection, each gate electrode for controlling the emission of electrons through the apertures along the gate electrode; and

wherein the anode electrode is configured as a plurality of strip-like anode electrodes
each extending in such a manner as to be opposed to the corresponding one of said gate
20 electrodes.

65. (new) An electron source as recited in claim 35,
wherein said nano-structures in the emitter layer are truncated to substantially the same
length, so that each exposed nano-structure in the gate aperture has substantially the same gate-
25 to-emitter distance.

66. (new) An electron source as recited in claim 65, wherein said nano-structures are truncated by chemical mechanical planarization.

30 67. (new) An electron source as recited in claim 35,

wherein said nano-structures are grown using a template, and said template is at least part of the embedding material.

68. (new) An electron source as recited in claim 54, wherein said supporting means is provided
5 by embedding portion of the nano-structure emitting means.

69. (new) An electron source as recited in claim 55, wherein said nano-structure emitting means each has substantially the same distance to the gating means.

10 70. (new) An electron source as recited in claim 59, wherein said nano-structures are truncated to substantially the same length.

71. (new) An display as recited in claim 60,

15 wherein said nano-structures in the emitter layer are truncated to substantially the same length, so that each exposed nano-structure in the gate aperture has substantially the same gate-to-emitter distance.

CONCLUSION

The supplemental amendment is summarized as the following:

Claims 34, 35, 60 are amended.

5 Claims 36-59, and 61- 64 are previously presented.

Claims 65 - 71 are newly added.

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Respectfully submitted

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